

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Shigeo Yoshi et al.

Serial No.: 09/895,213

Filed: July 2, 2001



Group Art Unit: 2826

Examiner: J. Mondt

For: SEMICONDUCTOR LIGHT-EMITTING DEVICE AND APPARATUS FOR DRIVING THE SAME

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Dear Sir:

Transmitted herewith is an Amendment in the above-identified application.



No additional fee is required.



Applicant is entitled to small entity status under 37 CFR 1.27



Also attached: Amendment, RCE, Revocation of Power of Attorney, Change of Address

The fee has been calculated as shown below:

	NO. OF CLAIMS	HIGHEST PREVIOUSLY PAID FOR	EXTRA CLAIMS	RATE	FEE
Total Claims	21	22	0	\$18.00 =	\$0.00
Independent Claims	1	3	0	\$84.00 =	\$0.00
Multiple claims newly presented					\$0.00
Total of Above Calculations					\$750.00

Please charge my Deposit Account No. 500417 in the amount of \$750.00. An additional copy of this transmittal sheet is submitted herewith.

The Commissioner is hereby authorized to charge payment of any fees associated with this communication or credit any overpayment, to Deposit Account No. 500417, including any filing fees under 37 CFR 1.16 for presentation of extra claims and any patent application processing fees under 37 CFR 1.17.

Respectfully submitted,

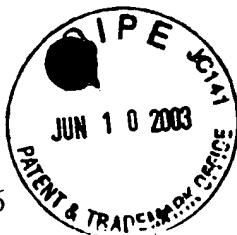
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Date: June 10, 2003

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PATENT TRADEMARK OFFICE

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JC 2800 MAIL ROOM



Docket No.: 60188-556

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For: SEMICONDUCTOR LIGHT-EMITTING DEVICE AND APPARATUS FOR DRIVING
THE SAME

AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In response to the Final Office action dated March 10, 2003, having a shortened statutory response period set to June 10, 2003, an RCE being filed herewith, please amend the above application as follows:

IN THE CLAIMS:

Please cancel claim 14 without prejudice or disclaimer.

Please amend claims 1, 15 and 20 as follows:

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- C'
1. (Twice Amended) A semiconductor light-emitting device comprising:
first and second semiconductor layers each of a first conductivity type;
a third semiconductor layer of a second conductivity type provided between the first and second semiconductor layers;